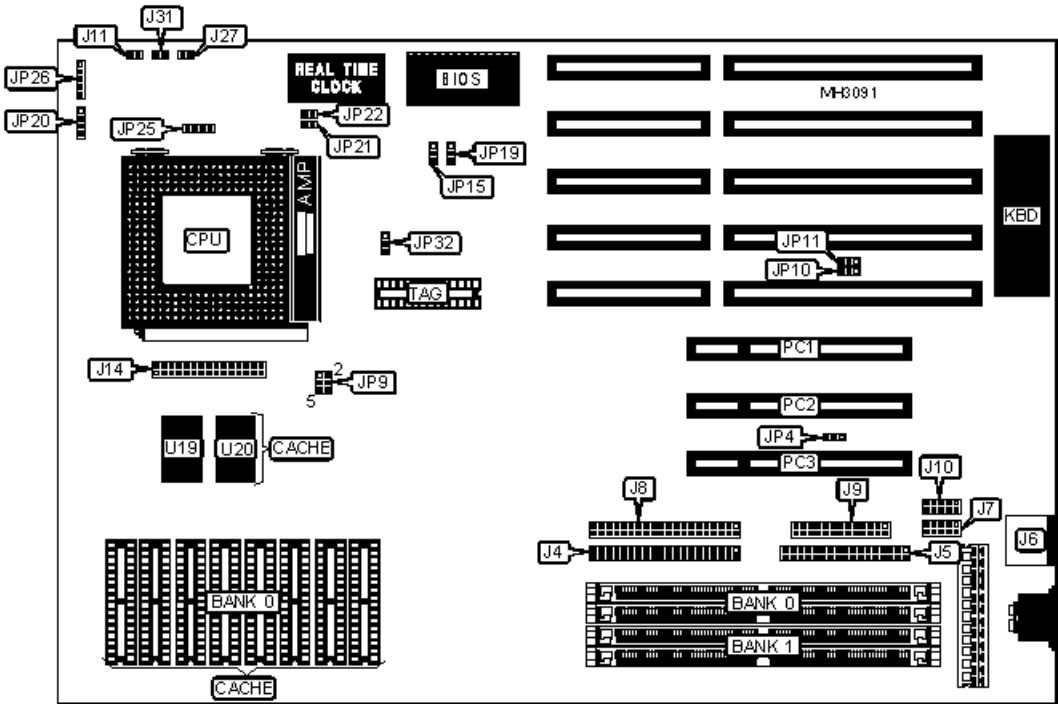


DTK COMPUTER, INC.

PAM-0054I

Processor	Pentium
Processor Speed	75/90/100/120/133MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VRM connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface 1	J4	VRM connector	J14
Floppy drive interface	J5	Reset switch	J27
PS/2 mouse port	J6	Turbo LED	J31
Serial port 1	J7	Speaker	JP20
IDE interface 2	J8	Chassis fan power	JP25

Parallel port	J9	Power LED & keylock	JP26
Serial port 2	J10	32-bit PCI slots	PC1 - PC3
IDE interface LED	J11		
Note: The location of pin 1 on JP25 is unidentified.			

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
On board I/O enabled	JP4	pins 1 & 2 closed
On board I/O disabled	JP4	pins 2 & 3 closed
Flash BIOS programming enabled	JP19	pins 1 & 2 closed
Flash BIOS programming disabled	JP19	pins 2 & 3 closed
» CMOS memory normal operation	JP21	Open
CMOS memory clear	JP21	Closed
» CMOS memory normal operation	JP22	Open
CMOS memory clear	JP22	Closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 1M x 36	(2) 256K x 36

10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
34MB	(2) 256K x 36	(2) 4M x 36

DRAM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
36MB	(2) 4M x 36	(2) 512K x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE

64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION				
Size	Bank 0	TAG	U19	U20
256KB (A)	(8) 32K x 8	(1) 8K or (1) 32K x 8	NONE	NONE
256KB (B)	NONE	NONE	(1) 32K x 32/36	(1) 32K x 32/36
512KB	(8) 64K x 8	(1) 32K x 8	NONE	NONE

Note: If U19 and U20 are used, Bank 0 must be empty.

CACHE JUMPER CONFIGURATION		
Size	JP15	JP32
None	N/A	pins 1 & 2 closed
256KB (A)	pins 1 & 2 closed	pins 1 & 2 closed
256KB (B)	pins 2 & 3 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION (MX8318)

Speed	JP9	JP21
75MHz	pins 5 & 6 closed	Open
90MHz	pins 3 & 4 closed	Open
100MHz	pins 1 & 2, 5 & 6 closed	Open
120MHz	pins 3 & 4 closed	Closed
133MHz	pins 1 & 2, 5 & 6 closed	Closed

CPU SPEED CONFIGURATION (IM1464)

Speed	JP9	JP21
75MHz	pins 1 & 2 closed	Open
90MHz	pins 1 & 2, 5 & 6 closed	Open
100MHz	pins 1 & 2, 3 & 4, 5 & 6 closed	Open
120MHz	pins 1 & 2, 5 & 6 closed	Closed
133MHz	pins 1 & 2, 3 & 4, 5 & 6 closed	Closed

DMA CONFIGURATION

DMA	JP10	JP11
DMA 1	pins 1 & 2 closed	pins 1 & 2 closed
DMA 3	pins 2 & 3 closed	pins 2 & 3 closed